

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): **Wu, et al.**

Docket No.: **APRI-P016**

Serial No.: (Not yet assigned)

Group Art Unit: (Not yet assigned)

Filed: **04/07/2004**

Examiner: (Not yet assigned)

Title: **Method and Apparatus for Selective, Incremental, Reconfigurable and Reusable Semiconductor Manufacturing Resolution-Enhancements**

INFORMATION DISCLOSURE STATEMENT

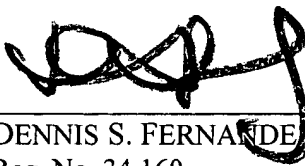
Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In keeping with the duty of candor and good faith owed to the Patent and Trademark Office, Applicants wish to bring the enclosed printed publications and U.S. patent references to the attention of the Examiner.

Applicant respectfully submits that Applicants' claimed invention is patentable over such references. Forms PTO/SB/08A and PTO/SB/08B listing these references are attached. It is understood the Office may find additionally relevant material in these or other references

Respectfully submitted,



DENNIS S. FERNANDEZ, ESQ.
Reg. No. 34,160

04/07/04

Date

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Substitute for form 1449/PTO

(Use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	Not yet assigned
Filing Date	04/07/2004
First Named Inventor	Wu, et al.
Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	APRI-P016

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature		Date Considered	
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Sheet	2	of	2	Application Number	Not yet assigned
				Filing Date	04/07/2004
				First Named Inventor	Wu, et al.
				Art Unit	Not yet assigned
				Examiner Name	Not yet assigned
				Attorney Docket Number	APRI-P016

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	SiVL-LRC - Silicon vs. Layout Verification System; http://www.synopsys.com/products/ntimrg/sivl_ds.html	

Examiner Signature		Date Considered	
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